|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| PROGRAM Zbirnik | Korak | Address  Vir naslova | ReadWrite  Vpis/branje RAM pomn. | dwrite  Vpis reg. Rd | RegSrc  Vhod v reg. | aluop  ALU Operac. | ADR  Sub Rx,PC,#odm | Irload  Vpis v uk. reg. IR | PC |
| Opis |  | **0..PC,**  **1..Rn** | **0..branje,**  **1..pisanje** | **0..ne,**  **1..da** | **0..Databus, 1..ALU, 2..Immediate** | **0..ADD,**  **2..SUB** | **0..ne,**  **1..da** | **0..ne,**  **1..da** | Naslov  ukaza |
| adr r0,STEV1 | (PC+4) Fetch |  |  |  |  |  |  | **1** | **0x2C** |
| *(sub r0,pc,#0x0c)* | Execute |  |  | **1** | **1** | **2** | **1** |  |  |
| ldr r1,[r0] | (PC+4) Fetch |  |  |  |  |  |  | **1** | **0x30** |
|  | Execute | **1** |  | **1** | **0** |  |  |  |  |
| adr r0,STEV2 | (PC+4) Fetch |  |  |  |  |  |  | **1** | **0x34** |
| *(sub r0,pc,#0x10)* | Execute |  |  | **1** | **1** | **2** | **1** |  |  |
| ldr r2,[r0] | (PC+4) Fetch |  |  |  |  |  |  | **1** | **0x38** |
|  | Execute | **1** |  | **1** | **0** |  |  |  |  |
| add r3,r1,r2 | (PC+4) Fetch |  |  |  |  |  |  | **1** | **0x3C** |
|  | Execute |  |  | **1** | **1** | **0** |  |  |  |
| adr r0,REZ | (PC+4) Fetch |  |  |  |  |  |  | **1** | **0x40** |
| *(sub r0,pc,#0x18)* | Execute |  |  | **1** | **1** | **2** | **1** |  |  |
| str r3,[r0] | (PC+4) Fetch |  |  |  |  |  |  | **1** | **0x44** |
|  | Execute | **1** | **1** |  |  |  |  |  |  |

***Mikro MiMo model CPE – krmilni signali v0***

Slika, ki vsebuje besede besedilo, diagram, posnetek zaslona, načrt

Opis je samodejno ustvarjen